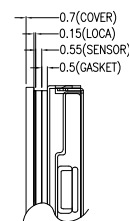
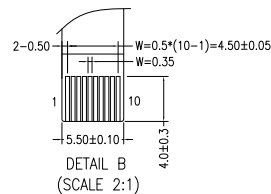
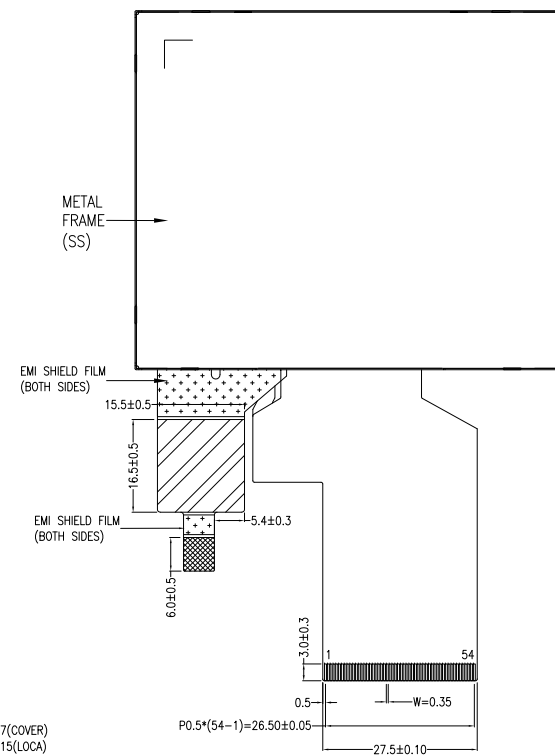
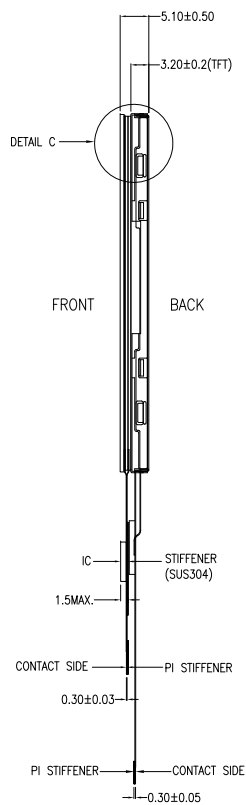


PIN ASSIGNMENT FOR LCM			
PIN	SYMBOL	PIN	SYMBOL
1	LEDK	28	RO
2	LEDK	29	R1
3	LEDA	30	R2
4	LEDA	31	R3
5	NC	32	R4
6	NC	33	R5
7	NC	34	R6
8	RESET	35	R7
9	SPENA	36	HSYNC
10	SPCLK	37	VSNC
11	SPDAT	38	DCLK
12	B0	39	NC
13	B1	40	NC
14	B2	41	VCC
15	B3	42	VCC
16	B4	43	NC
17	B5	44	NC
18	B6	45	NC
19	B7	46	NC
20	G0	47	NC
21	G1	48	SEL2
22	G2	49	SEL1
23	G3	50	SEL0
24	G4	51	NC
25	G5	52	DEN
26	G6	53	GND
27	G7	54	GND

PIN ASSIGNMENT FOR CTP			
PIN	SYMBOL	PIN	SYMBOL
1	VSS	6	NC
2	VDD	7	/RST
3	SCL	8	NC
4	NC	9	/INT
5	SDA	10	VSS



BACKLIGHT CIRCUIT DIAGRAM

- MI0350CFP-C3_(FT5446DQS-P03)_FIRMWARE_V05_20200713_ALL.BIN

1. 3.5 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VDD=2.8V~3.3V
4. DRIVER IC: FT5446DQS~P03, INTERFACE: IIC
5. RESOLUTION: 320*240
6. MULTI-FINGER: UP TO 5
7. HARDNESS OF COVER SURFACE: 7H (750G)
8. FIRMWARE FILE:

- | | | | |
|------|---------------------|---------|------------|
| 01 | FIRST ISSUE | RITA | 2024.09.26 |
| VER. | REVISED DESCRIPTION | REVISER | DATE |

 MULTI-INNO TECHNOLOGY CO.,LTD.			
DRAWN BY:	RITA	2024.09.26	SCALE : 1/1
CHECKED BY:			MODULE P/N:
APPROVED BY:			MI0350AG2T-3CP4
VERSION NO:	01		DESCRIPTION:
			TFT + CTP MODULE